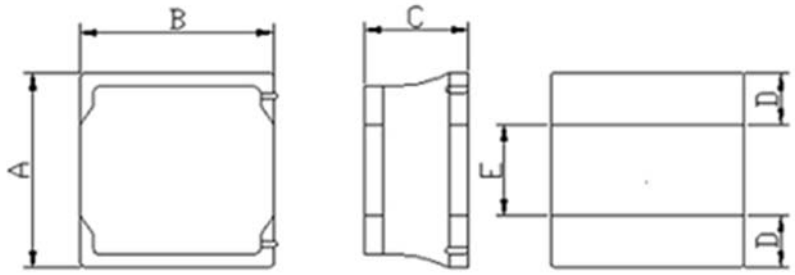


FEATRLRES

- This specification applies Low Profile Power Inductors.
- 100% Lead(Pb) & Halogen-Free and RoHS compliant.

CONFIGLRATIONS & DIMENSIONS (unit in mm)



Type	A	B	C	D	E	G	H	I
HPC4018TF	4.0±0.2	4.0±0.2	1.8 max.	1.2 ref.	1.6 ref.	-	-	-

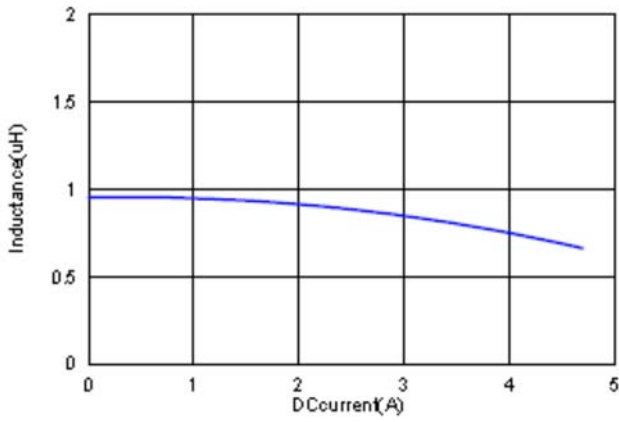
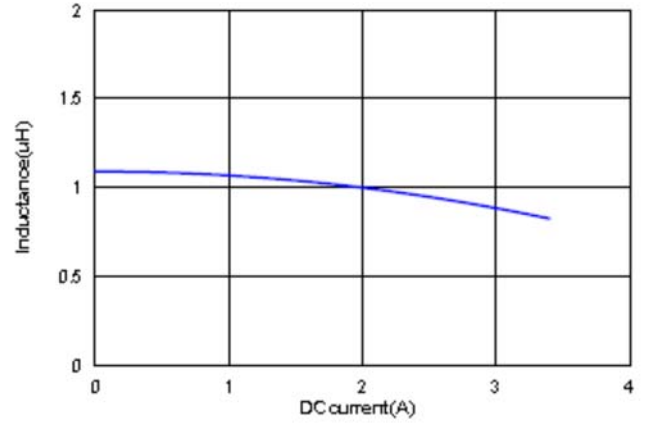
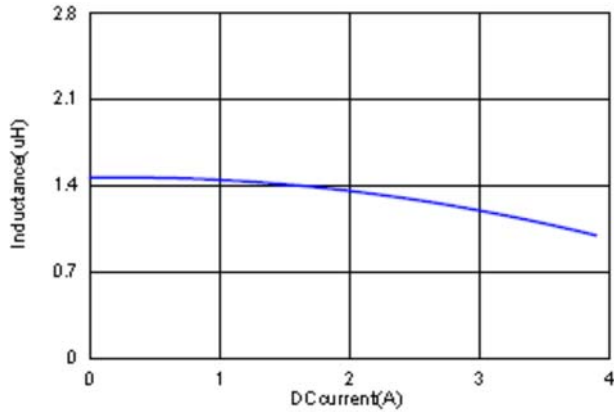
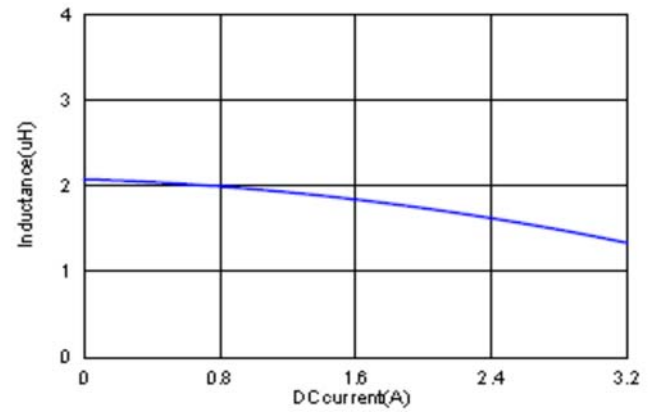
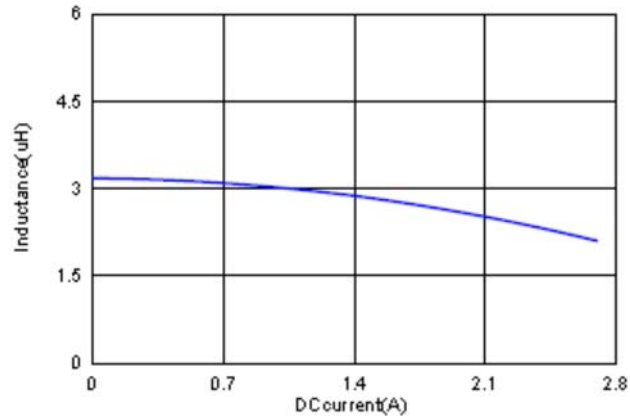
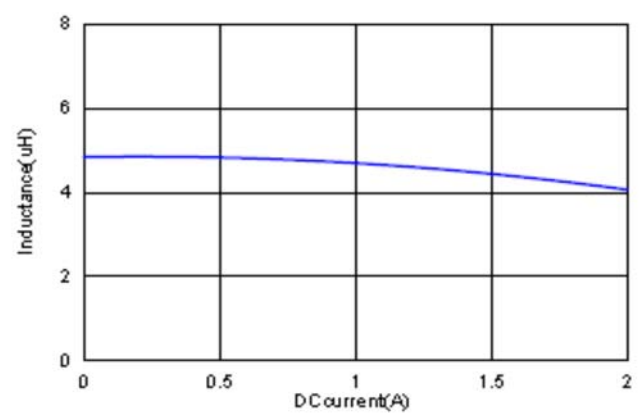
ELECTRICAL CHARACTERISTICS

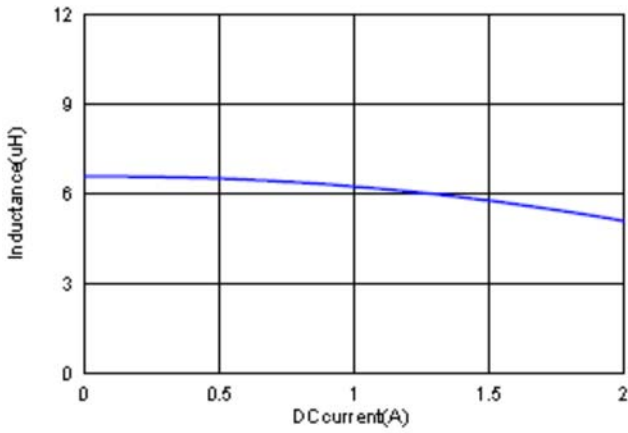
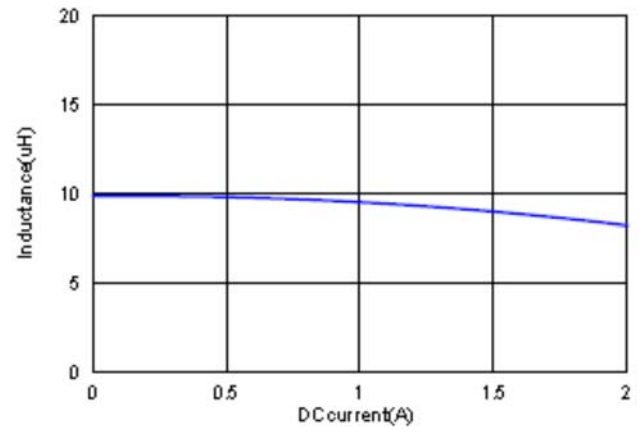
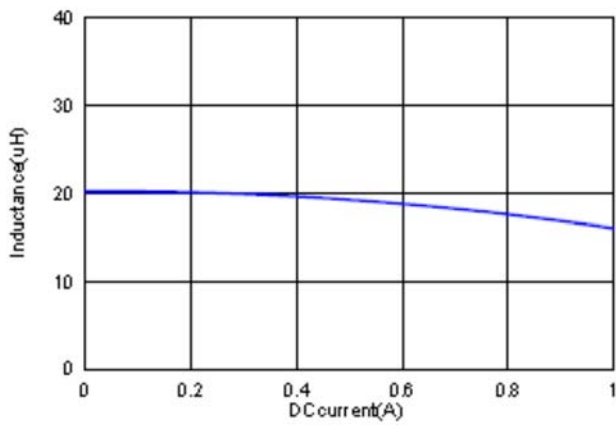
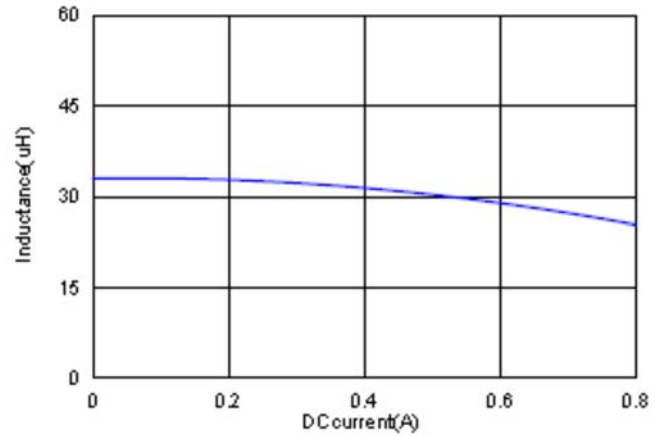
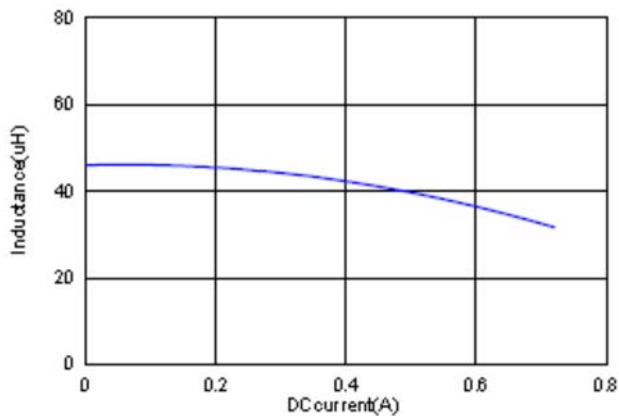
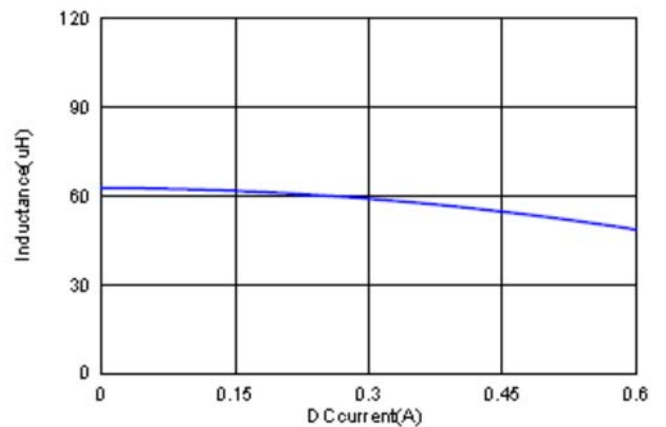
Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	SRF (MHz) typ.	DCR (Ω) ±20%	I sat (A)		I rms (A)	
						typ.	max.	typ.	max.
HNR4018BM-1R0M	1.0	±20%	1V100K	160	0.027	4.00	3.60	3.70	3.60
HNR4018BM-1R5M	1.5	±20%	1V100K	110	0.032	3.30	3.00	3.30	3.00
HNR4018BM-2R2M	2.2	±20%	1V100K	70	0.042	3.00	2.70	2.90	2.70
HNR4018BM-3R3M	3.3	±20%	1V100K	60	0.055	2.30	2.20	2.30	2.20
HNR4018BM-4R7M	4.7	±20%	1V100K	50	0.070	2.00	1.90	2.00	1.90
HNR4018BM-6R8M	6.8	±20%	1V100K	40	0.098	1.70	1.60	1.70	1.60
HNR4018BM-100M	10	±20%	1V100K	35	0.150	1.50	1.40	1.50	1.40
HNR4018BM-150M	15	±20%	1V100K	25	0.190	1.10	1.00	1.10	1.00
HNR4018BM-220M	22	±20%	1V100K	20	0.290	0.90	0.80	0.90	0.80
HNR4018BM-330M	33	±20%	1V100K	12	0.405	0.75	0.70	0.75	0.70
HNR4018BM-470M	47	±20%	1V100K	10	0.550	0.60	0.55	0.60	0.55
HNR4018BM-680M	68	±20%	1V100K	10	0.890	0.55	0.50	0.55	0.50
HNR4018BM-101M	100	±20%	1V100K	8	1.380	0.45	0.40	0.45	0.40
HNR4018BM-151M	150	±20%	1V100K	5	1.970	0.35	0.30	0.35	0.30
HNR4018BM-221M	220	±20%	1V100K	5	3.000	0.30	0.25	0.30	0.25

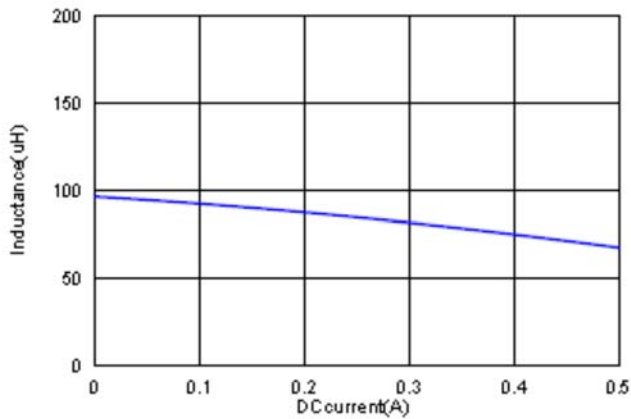
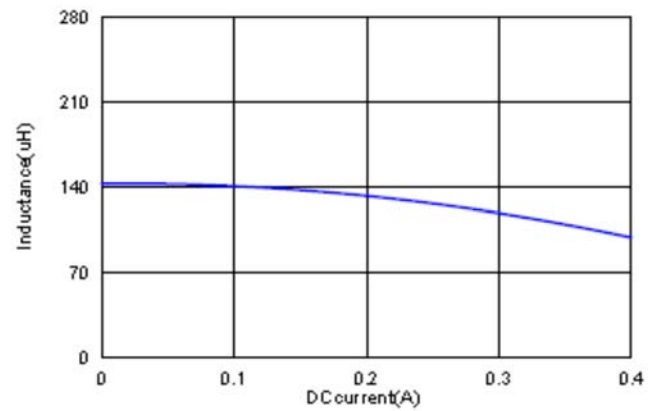
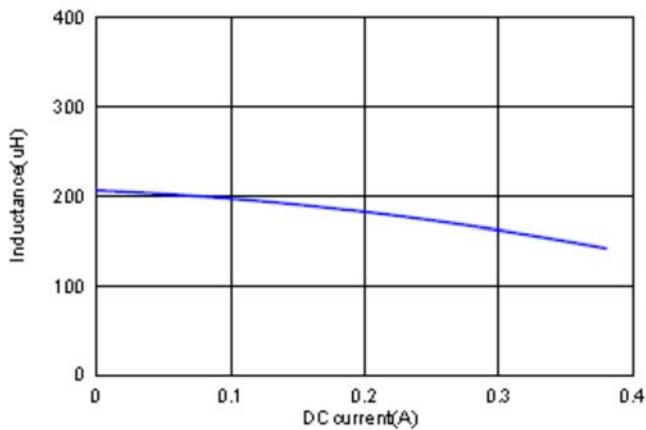
Note:

I_{sat} : Based on inductance change ($\Delta L/L_0 : \leq -30\%$) @ ambient temp. 25°C

I_{rms} : Based on temperature rise ($\Delta T : 40^\circ\text{C}$ typ.)

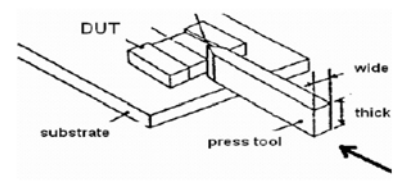
TYPICALELECTRICALCHARACTERISTICS:
HNR4018BM-1R0M

HNR4018BM -1R5M

HNR4018BM -2R2M

HNR4018BM -3R3M

HNR4018BM -4R7M

HNR4018BM -6R8M


HNR4018BM -100M

HNR4018BM -150M

HNR4018BM -220M

HNR4018BM -330M

HNR4018BM -470M

HNR4018BM -680M


HNR4018BM -101M

HNR4018BM -151M

HNR4018BM -221M


Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	1. -10~+40°C ,50~60%RH (Product with taping) 2. -40~+125°C (on board)	
Electrical Performance Test		
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR		CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately Δ L30%	Saturation DC Current (Isat) will cause L0 to drop Δ L(%)
Heat Rated Current (Irms)	Approximately Δ T40°C	Heat Rated Current (Irms) will cause the coil temperature rise Δ T(°C). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test	Appearance : No damage.	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature : 125 \pm 2°C (Inductor) Applied current : rated current Duration : 1000 \pm 12hrs Measured at room temperature after placing for 24 \pm 2 hrs

Load Humidity	Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity : 85±2 * R.H, Temperature : 85°C±2°C Duration : 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs															
Moisture Resistance		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Profiles 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs,keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.															
Thermal shock		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1 : -40±2°C 30±5min Step2 : 25±2°C ≤0.5min Step3 : 125±2°C 30±5min Number of cycles : 500 Measured at room temperature after placing for 24±2 hrs															
Vibration		Oscillation Frequency: 10 ~ 2K ~ 10Hz for 20 minutes Equipment : Vibration checker Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations).															
Bending		Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.															
Shock	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	<table border="1"> <thead> <tr> <th>Type</th> <th>Peak value (g's)</th> <th>Normal duration (D) (ms)</th> <th>Wave form</th> <th>Velocity change (Vi)/ft/sec</th> </tr> </thead> <tbody> <tr> <td>SMD</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> <tr> <td>Lead</td> <td>50</td> <td>11</td> <td>Half-sine</td> <td>11.3</td> </tr> </tbody> </table>	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)/ft/sec	SMD	50	11	Half-sine	11.3	Lead	50	11	Half-sine	11.3
Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)/ft/sec													
SMD	50	11	Half-sine	11.3													
Lead	50	11	Half-sine	11.3													
Solder ability	More than 95% of the terminal electrode should be covered with solder.	Preheat: 150°C,60sec.. Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C ◦ Flux for lead free: Rosin. 9.5% ◦ Dip time: 4±1sec ◦ Depth: completely cover the termination															
Resistance to Soldering Heat		Depth: completely cover the termination <table border="1"> <thead> <tr> <th>Temperature(°C)</th> <th>Time(s)</th> <th>Temperature ramp/immersion and emersion rate</th> <th>Number of heat cycles</th> </tr> </thead> <tbody> <tr> <td>260 ±5 (solder temp)</td> <td>10 ±1</td> <td>25mm/s ±6 mm/s</td> <td>1</td> </tr> </tbody> </table>	Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	1							
Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate	Number of heat cycles														
260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	1														
Terminal Strength	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg , <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 															

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.